

# Mechanising Turing Machines and Computability Theory in Isabelle



Jian Xu



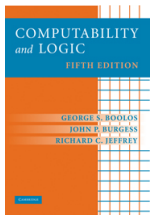
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# Why Turing Machines?

- At the beginning, it was just a student project about computability.



Computability and Logic (5th. ed)  
Boolos, Burgess and Jeffrey

- found an inconsistency in the definition of halting computations (Chap. 3 vs Chap. 8)

# Some Previous Works

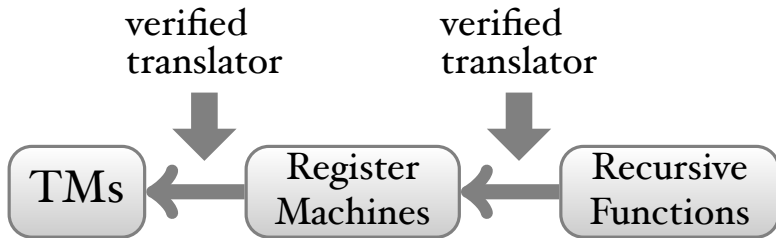
- Norrish formalised computability theory in HOL starting from the lambda-calculus
  - for technical reasons we could not follow him
  - some proofs use TMs (Wang tilings)
- Asperti and Ricciotti formalised TMs in Matita
  - no undecidability  $\Rightarrow$  interest in complexity
  - their UTM operates on a different alphabet than the TMs it simulates.

*"In particular, the fact that the universal machine operates with a different alphabet with respect to the machines it simulates is annoying." [Asperti and Ricciotti]*

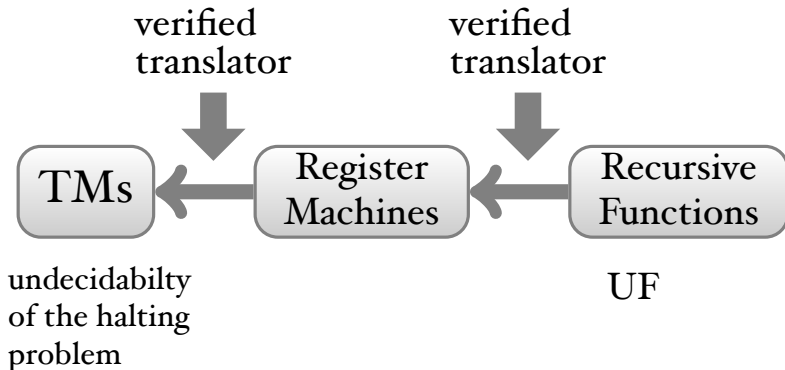
# The Big Picture



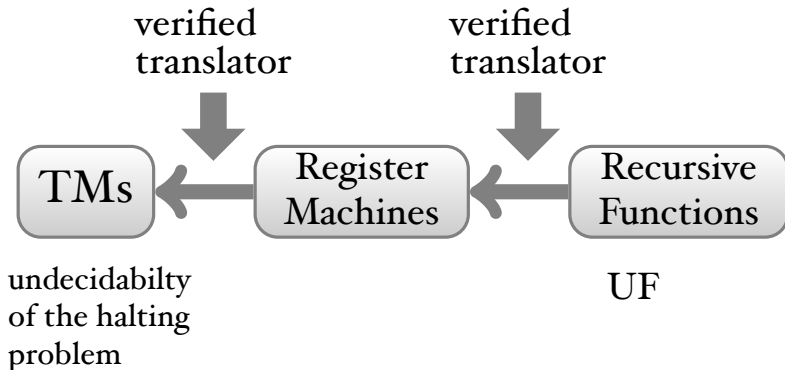
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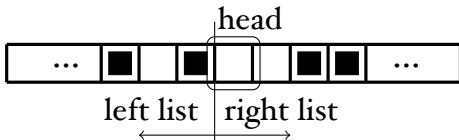
# The Big Picture



correct UTM by translation

# Turing Machines

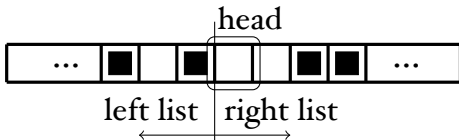
- tapes are lists and contain 0s or 1s only





# Turing Machines

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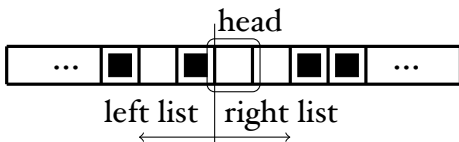


- *steps* function:

What does the TM calculate after it has executed  $n$  steps?

# Turing Machines

- tapes are lists and contain 0s or 1s only



- *steps* function:  
What does the TM calculate after it has executed  $n$  steps?
- designate the 0-state as "halting state" and remain there forever, i.e. have a *Nop*-action

# Register Machines

- programs are lists of instructions

$I ::= \text{Goto } L$       jump to instruction  $L$   
|  $\text{Inc } R$       increment register  $R$  by one  
|  $\text{Dec } R L$       if content of  $R$  is non-zero,  
                         then decrement it by one  
                         otherwise jump to instruction  $L$

# Register Machines

Spaghetti Code! instructions

$I ::=$	$Goto L$	jump to instruction $L$
	$Inc R$	increment register $R$ by one
	$Dec R L$	if content of $R$ is non-zero, then decrement it by one otherwise jump to instruction $L$

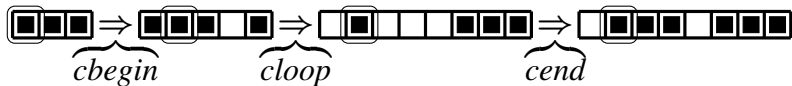
# Recursive Functions

$rec ::= Z$	zero-function
$S$	successor-function
$Id_m^n$	projection
$Cn^n f g s$	composition
$Pr^n f g$	primitive recursion
$Mn^n f$	minimisation

- $eval :: rec \Rightarrow nat\ list \Rightarrow nat$   
can be defined by simple recursion  
(HOL has *Least*)
- you define
  - addition, multiplication, logical operations, quantifiers...
  - coding of numbers (Cantor encoding), UTM

# Copy Turing Machine

- TM that copies a number on the input tape



$copy \stackrel{def}{=} cbegin ; cloop ; cend$

$cbegin \stackrel{def}{=}$

$[(W_0, 0), (R, 2), (R, 3),$   
 $(R, 2), (W_1, 3), (L, 4),$   
 $(L, 4), (L, 0)]$

$cloop \stackrel{def}{=}$

$[(R, 0), (R, 2), (R, 3),$   
 $(W_0, 2), (R, 3), (R, 4),$   
 $(W_1, 5), (R, 4), (L, 6),$   
 $(L, 5), (L, 6), (L, 1)]$

$cend \stackrel{def}{=}$

$[(L, 0), (R, 2), (W_1, 3),$   
 $(L, 4), (R, 2), (R, 2),$   
 $(L, 5), (W_0, 4), (R, 0),$   
 $(L, 5)]$

# Hoare Logic for TMs

- Hoare-triples

$$\{P\} p \{Q\} \stackrel{\text{def}}{=}$$

$\forall tp.$

if  $P$   $tp$  holds then

$\exists n.$  such that

$is\_final (steps (l, tp) p n) \wedge$

$Q$  holds\_for  $(steps (l, tp) p n)$

# Hoare Logic for TMs

- Hoare-triples and Hoare-pairs:

$$\{P\} p \{Q\} \stackrel{\text{def}}{=}$$

$\forall tp.$

if  $P$   $tp$  holds then

$\exists n.$  such that

$is\_final (steps (l, tp) p n) \wedge$

$Q$  holds\_for  $(steps (l, tp) p n)$

$$\{P\} p \uparrow \stackrel{\text{def}}{=}$$

$\forall tp.$

if  $P$   $tp$  holds then

$\forall n. \neg is\_final (steps (l, tp) p n)$



# Some Derived Rules

$$\frac{P' \mapsto P \quad \{P\} p \{Q\} \quad Q \mapsto Q'}{\{P'\} p \{Q'\}}$$

$$\frac{\{P\} p_1 \{Q\} \quad \{Q\} p_2 \{R\}}{\{P\} p_1 ; p_2 \{R\}} \quad \frac{\{P\} p_1 \{Q\} \quad \{Q\} p_2 \uparrow}{\{P\} p_1 ; p_2 \uparrow}$$

# Undecidability

*contra*  $\stackrel{\text{def}}{=} \text{copy} ; H ; \text{dither}$

# Undecidability

$contra \stackrel{def}{=} copy ; H ; dither$

- Suppose  $H$  decides  $contra$  called with code of  $contra$  halts, then

$$P_1 \stackrel{def}{=} \lambda tp. tp = ([], \langle code\ contra \rangle)$$

$$P_2 \stackrel{def}{=} \lambda tp. tp = ([0], \langle (code\ contra, code\ contra) \rangle)$$

$$P_3 \stackrel{def}{=} \lambda tp. \exists k. tp = (0^k, \langle 0 \rangle)$$

$$\frac{\frac{\{P_1\} copy \{P_2\} \quad \{P_2\} H \{P_3\}}{\{P_1\} copy ; H \{P_3\}} \quad \{P_3\} dither \uparrow}{\{P_1\} contra \uparrow}}$$

# Undecidability

$contra \stackrel{def}{=} copy ; H ; dither$

- Suppose  $H$  decides  $contra$  called with code of  $contra$  does *not* halt, then

$$Q_1 \stackrel{def}{=} \lambda tp. tp = ([], \langle code\ contra \rangle)$$

$$Q_2 \stackrel{def}{=} \lambda tp. tp = ([0], \langle (code\ contra, code\ contra) \rangle)$$

$$Q_3 \stackrel{def}{=} \lambda tp. \exists k. tp = (0^k, \langle 1 \rangle)$$

$$\frac{\frac{\{Q_1\} copy \{Q_2\} \quad \{Q_2\} H \{Q_3\}}{\{Q_1\} copy ; H \{Q_3\}} \quad \{Q_3\} dither \{Q_3\}}{\{Q_1\} contra \{Q_3\}}$$

# Hoare Reasoning

- reasoning is still quite demanding;  
the invariants of the copy-machine:

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$$I_1 n(l, r) \stackrel{\text{def}}{=} (l, r) = ([], I^n) \quad \text{(starting state)}$$

$$I_2 n(l, r) \stackrel{\text{def}}{=} \exists i j. 0 < i \wedge i + j = n \wedge (l, r) = (I^i, I^j)$$

$$I_3 n(l, r) \stackrel{\text{def}}{=} 0 < n \wedge (l, tl\ r) = (0::I^n, [])$$

$$I_4 n(l, r) \stackrel{\text{def}}{=} 0 < n \wedge (l, r) = (I^n, [0, 1]) \vee (l, r) = (I^{n-1}, [1, 0, 1])$$

$$I_0 n(l, r) \stackrel{\text{def}}{=} 1 < n \wedge (l, r) = (I^{n-2}, [1, 1, 0, 1]) \vee \quad \text{(halting state)} \\ n = 1 \wedge (l, r) = ([], [0, 1, 0, 1])$$

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$$J_1 n(l, r) \stackrel{\text{def}}{=} \exists i j. i + j + 1 = n \wedge (l, r) = (I^i, 1::1::0^j @ I^j) \wedge 0 < j \vee \\ 0 < n \wedge (l, r) = ([], 0::1::0^n @ I^n) \quad \text{(starting state)}$$

$$J_0 n(l, r) \stackrel{\text{def}}{=} 0 < n \wedge (l, r) = ([0], 1::0^n @ I^n) \quad \text{(halting state)}$$

---

$$K_1 n(l, r) \stackrel{\text{def}}{=} 0 < n \wedge (l, r) = ([0], 1::0^n @ I^n) \quad \text{(starting state)}$$

$$K_0 n(l, r) \stackrel{\text{def}}{=} 0 < n \wedge (l, r) = ([0], I^n @ 0::I^n) \quad \text{(halting state)}$$

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# Midway Conclusion

- feels awfully like reasoning about machine code
- compositional constructions / reasoning not frictionless
- sizes

sizes:

UF	140843 constructors
Reg. Mach.	2 Mio instructions
UTM	38 Mio states

\*old version: RM (12 Mio) UTM (112 Mio)

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- an observation: our treatment of recursive functions is a mini-version of the work by Myreen & Owens about deeply embedding HOL

# Separation Algebra

- introduced a separation algebra framework for register machines and TMs
- we can semi-automate the reasoning for our small TMs
- we can assemble bigger programs out of smaller components
- looks awfully like ``real'' assembly code



# Separation Algebra

- introduced a separation algebra framework for register machines and TMs
- we can semi-automate the reasoning for our small TMs
- we can assemble bigger programs out of smaller components
- looks awfully like ``real'' assembly code
- Conclusion: we have a playing ground for reasoning about low-level code; we work on automation