Mechanising Turing Machines and Computability Theory in Isabelle/HOL

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Abstract. We present a formalised theory of computability in the theorem prover Isabelle/HOL. This theorem prover is based on classical logic which precludes *direct* reasoning about computability: every boolean predicate is either true or false because of the law of excluded middle. The only way to reason about computability in a classical theorem prover is to formalise a concrete model for computation. We formalise Turing machines and relate them to abacus machines and recursive functions. We also formalise a universal Turing machine and Hoare-style reasoning techniques that allow us to reason about Turing machine programs. Our theory can be used to formalise other computability results. We give one example about the computational equivalence of single-sided Turing machines.

1 Introduction

Suppose you want to mechanise a proof for whether a predicate P, say, is decidable or not. Decidability of P usually amounts to showing whether $P \lor \neg P$ holds. But this does *not* work in Isabelle/HOL and other HOL theorem provers, since they are based on classical logic where the law of excluded middle ensures that $P \lor \neg P$ is always provable no matter whether P is constructed by computable means. We hit on this limitation previously when we mechanised the correctness proofs of two algorithms [7,8], but were unable to formalise arguments about decidability.

The only satisfying way out of this problem in a theorem prover based on classical logic is to formalise a theory of computability. Norrish provided such a formalisation for HOL4. He choose the λ -calculus as the starting point for his formalisation because of its "simplicity" [4, Page 297]. Part of his formalisation is a clever infrastructure for reducing λ -terms. He also established the computational equivalence between the λ -calculus and recursive functions. Nevertheless he concluded that it would be appealing to have formalisations for more operational models of computations, such as Turing machines or register machines. One reason is that many proofs in the literature use them. He noted however that [4, Page 310]:

"If register machines are unappealing because of their general fiddliness, Turing machines are an even more daunting prospect."

In this paper we take on this daunting prospect and provide a formalisation of Turing machines, as well as abacus machines (a kind of register machines) and recursive functions. To see the difficulties involved with this work, one has to understand that Turing

machine programs can be completely *unstructured*, behaving similar to Basic programs involving the infamous goto [3]. This precludes in the general case a compositional Hoare-style reasoning about Turing programs. We provide such Hoare-rules for when it *is* possible to reason in a compositional manner (which is fortunately quite often), but also tackle the more complicated case when we translate abacus programs into Turing programs. This reasoning about concrete Turing machine programs is usually left out in the informal literature, e.g. [2].

We are not the first who formalised Turing machines: we are aware of the work by Asperti and Ricciotti [1]. They describe a complete formalisation of Turing machines in the Matita theorem prover, including a universal Turing machine. However, they do *not* formalise the undecidability of the halting problem since their main focus is complexity, rather than computability theory. They also report that the informal proofs from which they started are not "sufficiently accurate to be directly usable as a guideline for formalization" [1, Page 2]. For our formalisation we follow mainly the proofs from the textbook [2] and found that the description there is quite detailed. Some details are left out however: for example, constructing the *copy Turing machine* is left as an excerise to the reader; also [2] only shows how the universal Turing machine is constructed for Turing machines computing unary functions. We had to figure out a way to generalise this result to *n*-ary functions. Similarly, when compiling recursive functions to abacus machines, the textbook again only shows how it can be done for 2- and 3-ary functions, but in the formalisation we need arbitrary functions. But the general ideas for how to do this are clear enough in [2].

The main difference between our formalisation and the one by Asperti and Ricciotti is that their universal Turing machine uses a different alphabet than the machines it simulates. They write [1, Page 23]:

"In particular, the fact that the universal machine operates with a different alphabet with respect to the machines it simulates is annoying."

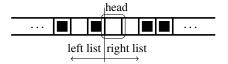
In this paper we follow the approach by Boolos et al [2], which goes back to Post [5], where all Turing machines operate on tapes that contain only *blank* or *occupied* cells. Traditionally the content of a cell can be any character from a finite alphabet. Although computationally equivalent, the more restrictive notion of Turing machines in [2] makes the reasoning more uniform. In addition some proofs *about* Turing machines are simpler. The reason is that one often needs to encode Turing machines—consequently if the Turing machines are simpler, then the coding functions are simpler too. Unfortunately, the restrictiveness also makes it harder to design programs for these Turing machines. In order to construct a universal Turing machine we therefore do not follow [1], instead follow the proof in [2] by translating abacus machines to Turing machines and in turn recursive functions to abacus machines. The universal Turing machine can then be constructed as a recursive function.

Contributions: We formalised in Isabelle/HOL Turing machines following the description of Boolos et al [2] where tapes only have blank or occupied cells. We mechanise the undecidability of the halting problem and prove the correctness of concrete Turing machines that are needed in this proof; such correctness proofs are left out in the informal literature. For reasoning about Turing machine programs we derive Hoare-rules.

We also construct the universal Turing machine from [2] by translating recursive functions to abacus machines and abacus machines to Turing machines. Since we have set up in Isabelle/HOL a very general computability model and undecidability result, we are able to formalise other results: we describe a proof of the computational equivalence of single-sided Turing machines, which is not given in [2], but needed for example for formalising the undecidability proof of Wang's tiling problem [6].

2 Turing Machines

Turing machines can be thought of as having a *head*, "gliding" over a potentially infinite tape. Boolos et al [2] only consider tapes with cells being either blank or occupied, which we represent by a datatype having two constructors, namely Bk and Oc. One way to represent such tapes is to use a pair of lists, written (l, r), where l stands for the tape on the left-hand side of the head and r for the tape on the right-hand side. We use the notation Bk^n (similarly Oc^n) for lists composed of n elements of n be also have the convention that the head, abbreviated n of the right list is the cell on which the head of the Turing machine currently scannes. This can be pictured as follows:



Note that by using lists each side of the tape is only finite. The potential infinity is achieved by adding an appropriate blank or occupied cell whenever the head goes over the "edge" of the tape. To make this formal we define five possible *actions* the Turing machine can perform:

$$a ::= W_{Bk}$$
 (write blank, Bk) | L (move left) | Nop (do-nothing operation) | W_{Oc} (write occupied, Oc) | R (move right)

We slightly deviate from the presentation in [2] (and also [1]) by using the *Nop* operation; however its use will become important when we formalise halting computations and also universal Turing machines. Given a tape and an action, we can define the following tape updating function:

$$\begin{array}{l} \textit{update}\;(l,r)\; \textit{W}_{\textit{Bk}}\;\stackrel{\textit{def}}{=}\;(l,\textit{Bk}::\textit{tl}\;r)\\ \textit{update}\;(l,r)\; \textit{W}_{\textit{Oc}}\;\stackrel{\textit{def}}{=}\;(l,\textit{Oc}::\textit{tl}\;r)\\ \textit{update}\;(l,r)\; L\;\;\stackrel{\textit{def}}{=}\;if\;l=[]\;\textit{then}\;([],\textit{Bk}::r)\;\textit{else}\;(\textit{tl}\;l,\textit{hd}\;l::r)\\ \textit{update}\;(l,r)\; R\;\;\stackrel{\textit{def}}{=}\;if\;r=[]\;\textit{then}\;(\textit{Bk}::l,[])\;\textit{else}\;(\textit{hd}\;r::l,\;\textit{tl}\;r)\\ \textit{update}\;(l,r)\;\textit{Nop}\;\stackrel{\textit{def}}{=}\;(l,r)\\ \end{array}$$

The first two clauses replace the head of the right list with a new Bk or Oc, respectively. To see that these two clauses make sense in case where r is the empty list, one has to

Next we need to define the *states* of a Turing machine. We follow the choice made in [1] by representing a state with a natural number and the states in a Turing machine program by the initial segment of natural numbers starting from θ . In doing so we can compose two Turing machine programs by shifting the states of one by an appropriate amount to a higher segment and adjusting some "next states" in the other.

An *instruction* of a Turing machine is a pair consisting of an action and a natural number (the next state). A *program p* of a Turing machine is then a list of such pairs. Using as an example the following Turing machine program, which consists of four instructions

$$dither \stackrel{def}{=} [\underbrace{(W_{Bk}, 1), (R, 2), (L, 1), (L, 0)}_{\text{1st state}}]$$

$$= \text{starting state}$$

$$= (1)$$

the reader can see we have organised our Turing machine programs so that segments of two belong to a state. The first component of such a segment determines what action should be taken and which next state should be transitioned to in case the head reads a Bk; similarly the second component determines what should be done in case of reading Oc. We have the convention that the first state is always the *starting state* of the Turing machine. The θ -state is special in that it will be used as the "halting state". There are no instructions for the θ -state, but it will always perform a Nop-operation and remain in the θ -state. Unlike Asperti and Riccioti [1], we have chosen a very concrete representation for programs, because when constructing a universal Turing machine, we need to define a coding function for programs. This can be directly done for our programs-as-lists, but is slightly more difficult for the functions used by Asperti and Ricciotti.

Given a program p, a state and the cell being read by the head, we need to fetch the corresponding instruction from the program. For this we define the function fetch

$$fetch \ p \ 0 \ _ = (Nop, 0)$$

$$fetch \ p \ (Suc \ s) \ Bk \stackrel{def}{=} case \ nth_of \ p \ (2 * s) \ of$$

$$None \Rightarrow (Nop, 0) \ | \ Some \ i \Rightarrow i$$

$$fetch \ p \ (Suc \ s) \ Oc \stackrel{def}{=} case \ nth_of \ p \ (2 * s + 1) \ of$$

$$None \Rightarrow (Nop, 0) \ | \ Some \ i \Rightarrow i$$

$$(2)$$

In this definition the function nth_of returns the nth element from a list, provided it exists (Some-case), or if it does not, it returns the default action Nop and the default state 0 (None-case). We often need to restrict Turing machine programs to be well-formed: a program p is well-formed if it satisfies the following three properties:

$$\textit{wf } p \stackrel{\textit{def}}{=} 2 \leq \textit{length } p \wedge \textit{is_even } (\textit{length } p) \wedge (\forall \, (a,s) \in \textit{p. } s \leq \textit{length } p \textit{ div } 2)$$

The first states that p must have at least an instruction for the starting state; the second that p has a Bk and Oc instruction for every state, and the third that every next-state is one of the states mentioned in the program or being the 0-state.

We need to be able to sequentially compose Turing machine programs. Given our concrete representation, this is relatively straightforward, if slightly fiddly. We use the following two auxiliary functions:

shift
$$p$$
 $n \stackrel{def}{=} map(\lambda(a, s). (a, if s = 0 then 0 else $s + n)) p$
adjust $p \stackrel{def}{=} map(\lambda(a, s). (a, if s = 0 then Suc (length $p \ div \ 2) else \ s)) p$$$

The first adds n to all states, exept the θ -state, thus moving all "regular" states to the segment starting at n; the second adds Suc ($length \ p \ div \ 2$) to the θ -state, thus redirecting all references to the "halting state" to the first state after the program p. With these two functions in place, we can define the $sequential\ composition$ of two Turing machine programs p_1 and p_2 as

$$p_1 \oplus p_2 \stackrel{def}{=} adjust \ p_1 \ @ shift \ p_2 \ (length \ p_1 \ div \ 2)$$

A configuration c of a Turing machine is a state together with a tape. This is written as (s, (l, r)). We say a configuration is final if s = 0 and we say a predicate P holds for a configuration if P holds for the tape (l, r). If we have a configuration and a program, we can calculate what the next configuration is by fetching the appropriate action and next state from the program, and by updating the state and tape accordingly. This single step of execution is defined as the function step

$$step(s, (l, r)) p \stackrel{def}{=} let(a, s') = fetch p s (read r)$$

 $in(s', update(l, r) a)$

where $read\ r$ returns the head of the list r, or if r is empty it returns Bk. It is impossible in Isabelle/HOL to lift the step-function to realise a general evaluation function for Turing machines. The reason is that functions in HOL-based provers need to be terminating, and clearly there are Turing machine programs that are not. We can however define an evaluation function that performs exactly n steps:

$$steps \ c \ p \ 0 \qquad \stackrel{def}{=} \ c$$
$$steps \ c \ p \ (Suc \ n) \stackrel{def}{=} \ steps \ (step \ c \ p) \ p \ n$$

Recall our definition of fetch (shown in (2)) with the default value for the θ -state. In case a Turing program takes according to the usual textbook definition [2] less than n steps before it halts, then in our setting the steps-evaluation does not actually halt, but rather transitions to the θ -state (the final state) and remains there performing Nop-actions until n is reached.

We often need to restrict tapes to be in *standard form*, which means the left list of the tape is either empty or only contains Bks, and the right list contains some "clusters" of Ocs separted by single blanks. To make this formal we define the following function

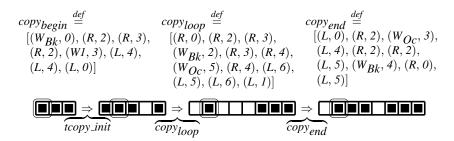


Fig. 1. The three components of the *copy Turing machine* (above). If started (below) with the tape ($[], \langle [2] \rangle$) the first machine appends [Bk, Oc] at the end of the right tape; the second then "moves" all Ocs except the first from the beginning of the tape to the end; the third "refills" the original block of Ocs. The resulting tape is ($[Bk], \langle [2, 2] \rangle$).

$$\begin{split} \langle [] \rangle & \stackrel{def}{=} [] \\ \langle [n] \rangle & \stackrel{def}{=} Oc^{n+1} \\ \langle n :: ns \rangle & \stackrel{def}{=} Oc^{n+1} @ [Bk] @ \langle ns \rangle \end{split}$$

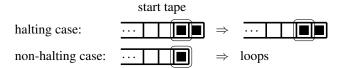
A standard tape is then of the form $(Bk^l, \langle [n_1,...,n_m] \rangle)$ for some l and n_i . Note that the head in a standard tape "points" to the leftmost Oc on the tape. Note also that the natural number O is represented by a single filled cell on a standard tape, I by two filled cells and so on.

Before we can prove the undecidability of the halting problem for our Turing machines, we need to analyse two concrete Turing machine programs and establish that they are correct—that means they are "doing what they are supposed to be doing". Such correctness proofs are usually left out in the informal literature, for example [2]. One program we need to prove correct is the *dither* program shown in (1) and the other program is *copy* defined as

$$copy \stackrel{def}{=} copy_{begin} \oplus copy_{loop} \oplus copy_{end}$$
 (3)

Like Asperti and Ricciotti with their notion of realisability, we have set up our Hoarerules so that we can deal explicitly with total correctness and non-terminantion, rather than have notions for partial correctness and termination. Although the latter would allow us to reason more uniformly (only using Hoare-triples), we prefer our definitions because we can derive simple Hoare-rules for sequentially composed Turing programs. In this way we can reason about the correctness of $copy_{begin}$, for example, completely separately from $copy_{loop}$ and $copy_{end}$.

It is realatively straightforward to prove that the Turing program dither shown in (1) is correct. This program should be the "identity" when started with a standard tape representing I but loop when started with θ instead, as pictured below.



We can prove the following Hoare-statements:

$$\{\lambda tp. \ \exists n. \ tp = (Bk^n, \langle I \rangle)\}\ dither \ \{\lambda tp. \ \exists n. \ tp = (Bk^n, \langle I \rangle)\}\ \{\lambda tp. \ \exists n. \ tp = (Bk^n, \langle O \rangle)\}\ dither \ \uparrow$$

The first is by a simple calculation. The second is by induction on the number of steps we can perform starting from the input tape.

The purpose of the *copy* program defined in (3) is to produce the standard tape $(-, \langle [n, n] \rangle)$ when started with $(-, \langle [n] \rangle)$, that is making a copy of a value on the tape. Reasoning about this program is substantially harder than about *dither*. To ease the burden, we prove the following two Hoare-rules for sequentially composed programs.

$$\frac{\{P\}\,p_1\;\{Q\}\quad\{Q\}\,p_2\;\{R\}}{\{P\}\,p_1\oplus p_2\;\{R\}}\,wfp_1\qquad \frac{\{P\}\,p_1\;\{Q\}\quad\{Q\}\,p_2\uparrow}{\{P\}\,p_1\oplus p_2\uparrow}\,wfp_1$$

The first corresponds to the usual Hoare-rule for composition of two terminating programs. The second rule is for cases where the first program terminates generating a tape for which the second program loops. The side-condition about $wf p_1$ is needed in both rules in order to ensure that the redirection of the halting and initial state in p_1 and p_2 match up correctly.

The Hoare-rules above allow us to prove the correctness of copy by considering the correctness of $copy_{begin}$, $copy_{loop}$ and $copy_{end}$ in isolation. We will show the details for the program $copy_{begin}$.

measure for the copying TM, which we however omit.

stdhalt
$$p$$
 ns $\stackrel{def}{=} \{ \lambda t p. t p = ([], \langle ns \rangle) \} p \{ \lambda t p. \exists k n. t p = (Bk^k, \langle n \rangle) \}$

Undecidability of the halting problem.

We assume a coding function from Turing machine programs to natural numbers. $stdhalt\ M\ lm$ implies $\{\lambda tp.\ tp = ([Bk], \langle code\ M::lm\rangle)\}\ H\ \{\lambda tp.\ \exists\ n.\ tp = (Bk^n, \langle 0\rangle)\}\ \neg\ stdhalt\ M\ lm$ implies $\{\lambda tp.\ tp = ([Bk], \langle code\ M::lm\rangle)\}\ H\ \{\lambda tp.\ \exists\ n.\ tp = (Bk^n, \langle I\rangle)\}$

Then contradiction

$$tcontra \stackrel{def}{=} copy \oplus H \oplus dither$$

Proof

$$\begin{array}{c} \{P_{1}\} \ copy \ \{P_{2}\} \quad \{P_{2}\} \ H \ \{P_{3}\} \\ \hline \{P_{1}\} \ copy \oplus H \ \{P_{3}\} \\ \hline \{P_{1}\} \ tcontra \ \{P_{3}\} \\ \hline \\ \{P_{1}\} \ tcopy \ \{P_{2}\} \quad \{P_{2}\} \ H \ \{Q_{3}\} \\ \hline \\ \{P_{1}\} \ copy \oplus H \ \{Q_{3}\} \\ \hline \\ \{P_{1}\} \ tcontra \ \uparrow \end{array}$$

Magnus: invariants – Section 5.4.5 on page 75.

3 Abacus Machines

Boolos et al [2] use abacus machines as a stepping stone for making it less laborious to write programs for Turing machines. Abacus machines operate over an unlimited number of registers R_0, R_1, \ldots each being able to hold an arbitrary large natural number. We use natural numbers to refer to registers, but also to refer to *opcodes* of abacus machines. Obcodes are given by the datatype

```
o ::= Inc R increment register R by one \mid Dec R o if content of R is non-zero, then decrement it by one otherwise jump to opcode o \mid Goto o jump to opcode o
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A program of an abacus machine is a list of such obcodes. For example the program clearing the register R (setting it to 0) can be defined as follows:

The second opcode Goto (0::'a) in this program means we jump back to the first opcode, namely $Dec\ R\ o$. The $memory\ m$ of an abacus machine holding the values of the registers is represented as a list of natural numbers. We have a lookup function for this memory, written $abc_lm_v\ m\ R$, which looks up the content of register R; if R is not in this list, then we return 0. Similarly we have a setting function, written $abc_lm_s\ m\ R\ n$, which sets the value of R to n, and if R was not yet in m it pads it approriately with 0s.

Abacus machine halts when it jumps out of range.

4 Recursive Functions

5 Wang Tiles

Used in texture mapings - graphics

6 Related Work

The most closely related work is by Norrish [4], and Asperti and Ricciotti [1]. Norrish bases his approach on lambda-terms. For this he introduced a clever rewriting technology based on combinators and de-Bruijn indices for rewriting modulo β -equivalence (to keep it manageable)

Given some input tape (l_i,r_i) , we can define when a program p generates a specific output tape (l_o,r_o)

runs
$$p(l_i, r_i) (l_o, r_o) \stackrel{def}{=}$$

 $\exists n. \ nsteps (l, (l_i, r_i)) \ p \ n = (0, (l_o, r_o))$

where l stands for the starting state and 0 for our final state. A program p with input tape (l_i, r_i) halts iff

halts
$$p(l_i, r_i) \stackrel{\text{def}}{=} \exists l_o r_o$$
. runs $p(l_i, r_i) (l_o, r_o)$

Later on we need to consider specific Turing machines that start with a tape in standard form and halt the computation in standard form. To define a tape in standard form, it is useful to have an operation that translates lists of natural numbers into tapes.

By this we mean

This means the Turing machine starts with a tape containg n Ocs and the head pointing to the first one; the Turing machine halts with a tape consisting of some Bks, followed by a "cluster" of Ocs and after that by some Bks. The head in the output is pointing again at the first Oc. The intuitive meaning of this definition is to start the Turing machine with a tape corresponding to a value n and producing a new tape corresponding to the value l (the number of Ocs clustered on the output tape).

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