# **POSIX Lexing with Bitcoded Derivatives**

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#### Abstract -

Sulzmann and Lu described a lexing algorithm that calculates Brzozowski derivatives using bit-sequences annotated to regular expressions. Their algorithm generates POSIX values which encode the information of *how* a regular expression matches a string—that is, which part of the string is matched by which part of the regular expression. The purpose of the bit-sequences in Sulzmann and Lu's algorithm is to keep the size of derivatives small which is achieved by 'aggressively' simplifying regular expressions. In this paper we describe a slight variant of Sulzmann and Lu's algorithm and (i) prove that this algorithm generates unique POSIX values; (ii) we also establish a cubic bound for the size of the derivatives—in earlier works, derivatives can grow exponentially even after simplification.

**2012 ACM Subject Classification** Design and analysis of algorithms; Formal languages and automata theory

Keywords and phrases POSIX matching, Derivatives of Regular Expressions, Isabelle/HOL

Digital Object Identifier 10.4230/LIPIcs...

## 1 Introduction

Brzozowski [3] introduced the notion of the *derivative*  $r \setminus c$  of a regular expression r w.r.t. a character c, and showed that it gave a simple solution to the problem of matching a string s with a regular expression r: if the derivative of r w.r.t. (in succession) all the characters of the string matches the empty string, then r matches s (and *vice versa*). The derivative has the property (which may almost be regarded as its specification) that, for every string s and regular expression r and character c, one has  $cs \in L$  r if and only if  $s \in L$  ( $r \setminus c$ ). The beauty of Brzozowski's derivatives is that they are neatly expressible in any functional language, and easily definable and reasoned about in theorem provers—the definitions just consist of inductive datatypes and simple recursive functions. A mechanised correctness proof of Brzozowski's matcher in for example HOL4 has been mentioned by Owens and Slind [9]. Another one in Isabelle/HOL is part of the work by Krauss and Nipkow [6]. And another one in Coq is given by Coquand and Siles [4].

If a regular expression matches a string, then in general there is more than one way of how the string is matched. There are two commonly used disambiguation strategies to generate a unique answer: one is called GREEDY matching [5] and the other is POSIX matching [1, 7, 8, 10, 11]. For example consider the string xy and the regular expression  $(x + y + xy)^*$ . Either the string can be matched in two 'iterations' by the single letter-regular expressions x and y, or directly in one iteration by xy. The first case corresponds to GREEDY matching, which first matches with the left-most symbol and only matches the next symbol in case of a mismatch (this is greedy in the sense of preferring instant gratification to delayed repletion). The second case is POSIX matching, which prefers the longest match.

## 2 Background

Sulzmann-Lu algorithm with inj. State that POSIX rules. metion slg is correct.

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$$r_{1} \xrightarrow{-\backslash a} r_{2} \xrightarrow{-\backslash b} r_{3} \xrightarrow{-\backslash c} r_{4} \text{ nullable}$$

$$v_{1} \longleftrightarrow v_{2} \longleftrightarrow v_{3} \longleftrightarrow v_{4}$$

$$inj r_{1} a v_{2} \longleftrightarrow inj r_{2} b v_{3} \longleftrightarrow inj r_{3} c$$

**Figure 1** The two phases of the algorithm by Sulzmann & Lu [10], matching the string [a, b, c]. The first phase (the arrows from left to right) is Brzozowski's matcher building successive derivatives. If the last regular expression is *nullable*, then the functions of the second phase are called (the top-down and right-to-left arrows): first *mkeps* calculates a value  $v_4$  witnessing how the empty string has been recognised by  $r_4$ . After that the function *inj* "injects back" the characters of the string into the values.

$$\frac{s \in r_1 \rightarrow v}{s \in (r_1 + r_2) \rightarrow val.Left \ v} P+L \qquad \frac{s \in r_2 \rightarrow v \qquad s \notin L \ r_1}{s \in (r_1 + r_2) \rightarrow val.Left \ v} P+R$$

$$\frac{s_1 \in r_1 \rightarrow v_1 \qquad s_2 \in r_2 \rightarrow v_2}{\sharp s_3 \ s_4. \ s_3 \neq [] \land s_3 \ @ \ s_4 = s_2 \land s_1 \ @ \ s_3 \in L \ r_1 \land s_4 \in L \ r_2}{(s_1 \ @ \ s_2) \in (r_1 \cdot r_2) \rightarrow Seq \ v_1 \ v_2} PS$$

$$\frac{\sharp s_3 \ s_4. \ s_3 \neq [] \land s_3 \ @ \ s_4 = s_2 \land s_1 \ @ \ s_3 \in L \ r_1 \land s_4 \in L \ r_2}{[] \in (r^*) \rightarrow Stars \ []} P[]$$

$$\frac{\sharp s_3 \ s_4. \ s_3 \neq [] \land s_3 \ @ \ s_4 = s_2 \land s_1 \ @ \ s_3 \in L \ r \land s_4 \in L \ (r^*)}{(s_1 \ @ \ s_2) \in (r^*) \rightarrow Stars \ (v :: vs)} P_{\star}$$

**Figure 2** Our inductive definition of POSIX values.

### 3 Bitcoded Derivatives

bitcoded regexes / decoding / bmkeps gets rid of the second phase (only single phase) correctness

## 4 Simplification

not direct correspondence with PDERs, because of example problem with retrieve correctness

- 5 Bound NO
- 6 Bounded Regex / Not
- 7 Conclusion

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```
ASEQ bs AZERO r2.0 → AZERO
                                                                                                                                                                                                                                                               ASEQ bs r1.0 AZERO → AZERO
                                                                                                                                                                                                                                                                                                                                                                                                                                                         ASEQ bs1.0 (AONE bs2.0) r \rightsquigarrow fuse (bs1.0 @ le base (bs1.
                                                                                                                                                                                                                                                      r1.0 \leadsto r2.0
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    r3.0 \leadsto r4.0
                                                                                                                                                                                                                                                                                                                                                                                                              ASEQ bs r1.0 \ r3.0 \rightsquigarrow ASEQ \ bs \ r1.0 \ r4.0
                                                                                                                                                                                ASEQ bs r1.0 r3.0 \sim ASEQ bs r2.0 r3.0
                                                                                                                                                                                                                                                                   AALTs\ bs\ [] \rightsquigarrow AZERO
                                                                                                                                                                                                                                                                                                                                                                                                        AALTs bs [r] \rightsquigarrow fuse bs r
                                                                                                                                                                                                                                                                                                                                                                rs1.0 s → rs2.0
                                                                                                                                                                                                                                                                                                                AALTs bs rs1.0 \sim AALTs bs rs2.0
                                                                                                                                                                                                                                                                                                                    rs1.0 s \leadsto rs2.0
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 r1.0 \leadsto r2.0
                                                                                                                                                                                                                                                                                            (r::rs1.0) s \leftrightarrow (r::rs2.0)
                                                                                                                                                                                                                                                                                                                                                                                                                                                   (r1.0::rs) s \leadsto (r2.0::rs)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          erase\ a1.0 = erase\ a2.0
(AZERO :: rs) s \leadsto rs
                                                                                                                               (AALTs\ bs1.0\ rs1.0::rsb)\ s \leadsto (map\ (fuse\ bs1.0)\ rs1.0\ @\ rsb)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                    (rsa @ [a1.0] @ rsb @ [a2.0] @ rsc) s \leadsto (rsa
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**Figure 3** ???

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